



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of:

Richard B. Keller

Application No.: 09/884,312

Filed: 06/18/2001

For: Fast Cyclical Redundancy Check
(CRC) Generation

Examiner: John P. Trimmings

Art Unit: 2133

Confirmation No.: 4827

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on this date: 05/05/2004

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AMENDMENT AND RESPONSE TO OFFICE ACTION

INTRODUCTORY COMMENTS

This communication is submitted in response to the Office Action mailed February 6, 2004. In this Office Action claims 1, 2, 4, 5, 7, 12, 13, 15, 16, and 18 are rejected under 35 USC 102(b) as being anticipated by "High Speed Parallel Cyclic Redundancy Check Generator," IBM Technical Disclosure Bulletin NN901051, Vol. 22, Issue 5, pp51-56, October 1, 1990 (hereinafter, "NN901051"); claims 10, 20, 21, 23, 24, and 26 are rejected under 35 USC 103(a) as being unpatentable over NN901051 in view of "Parallel CRC Generation," Guido Albertango et al., IEEE Micro 0272-1732/90/1000-0063501.00, October, 1990, pp 63-71 (hereinafter "Albertango"); claim 11 is rejected under 35 USC 103(a) as being unpatentable over NN901051 in view of Huber Shih et al., U.S. Patent No. 4937828 (hereinafter "Shih"); the drawings are objected to; and claims 3, 6, 8, 9, 14, 17, 19, 22, 25, and 27 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in

independent form. Reconsideration of the above captioned application in view of the amendments and remarks to follow is respectfully requested.

Claims 1-27 are pending. None of the claims are currently amended.